FIG. 1

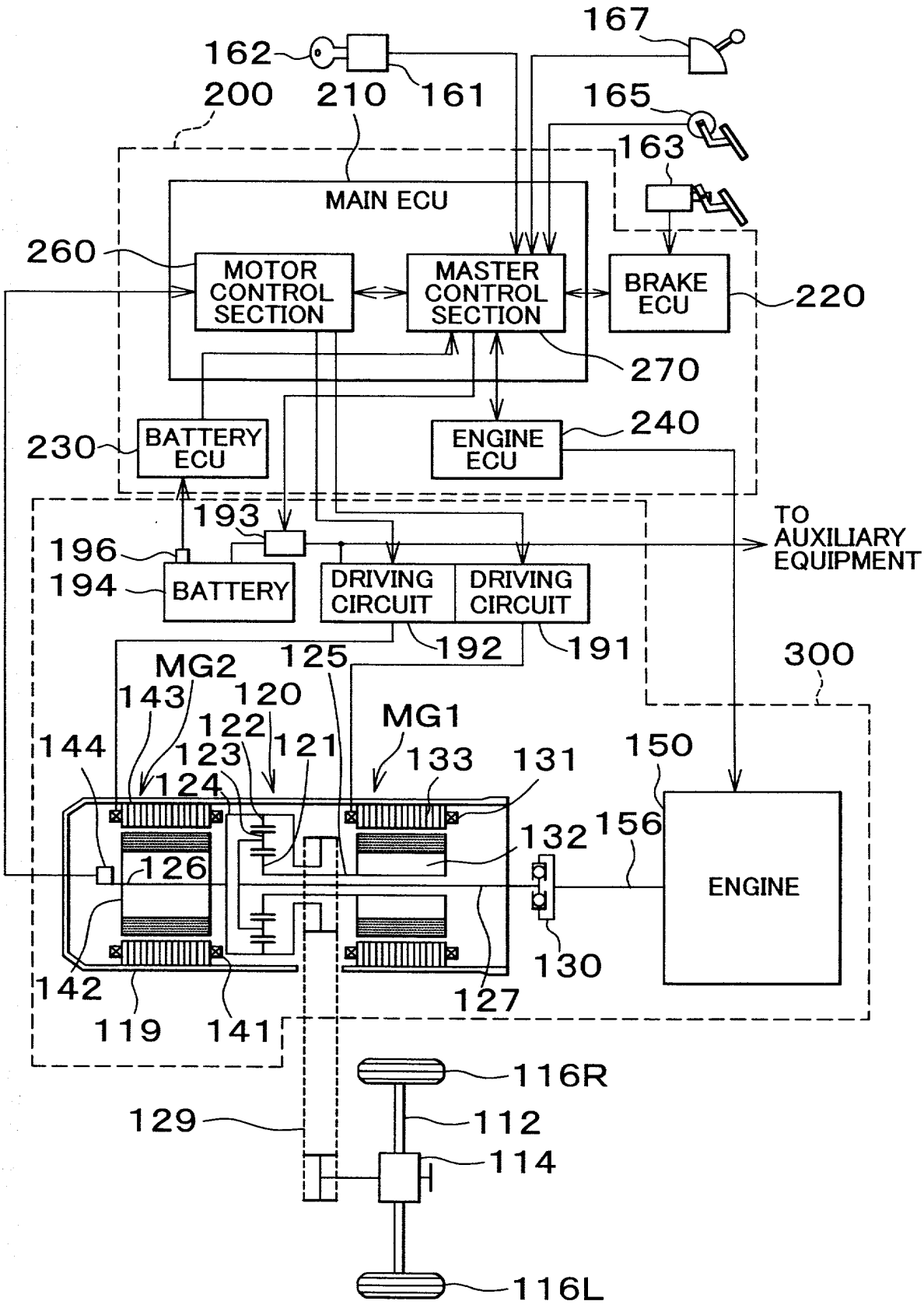

167
162
161
200
210
165
163
MAIN ECU
260
MOTOR CONTROL SECTION
MASTER CONTROL SECTION
BRAKE ECU
220
270
230
BATTERY ECU
ENGINE ECU
240
193
196
194
BATTERY
DRIVING CIRCUIT
DRIVING CIRCUIT
TO AUXILIARY EQUIPMENT
192
191
300
125
MG2
143
120
122
MG1
144
123
121
133
131
150
124
132
156
126
ENGINE
130
142
127
119
141
116R
129
112
114
116L

FIG. 2

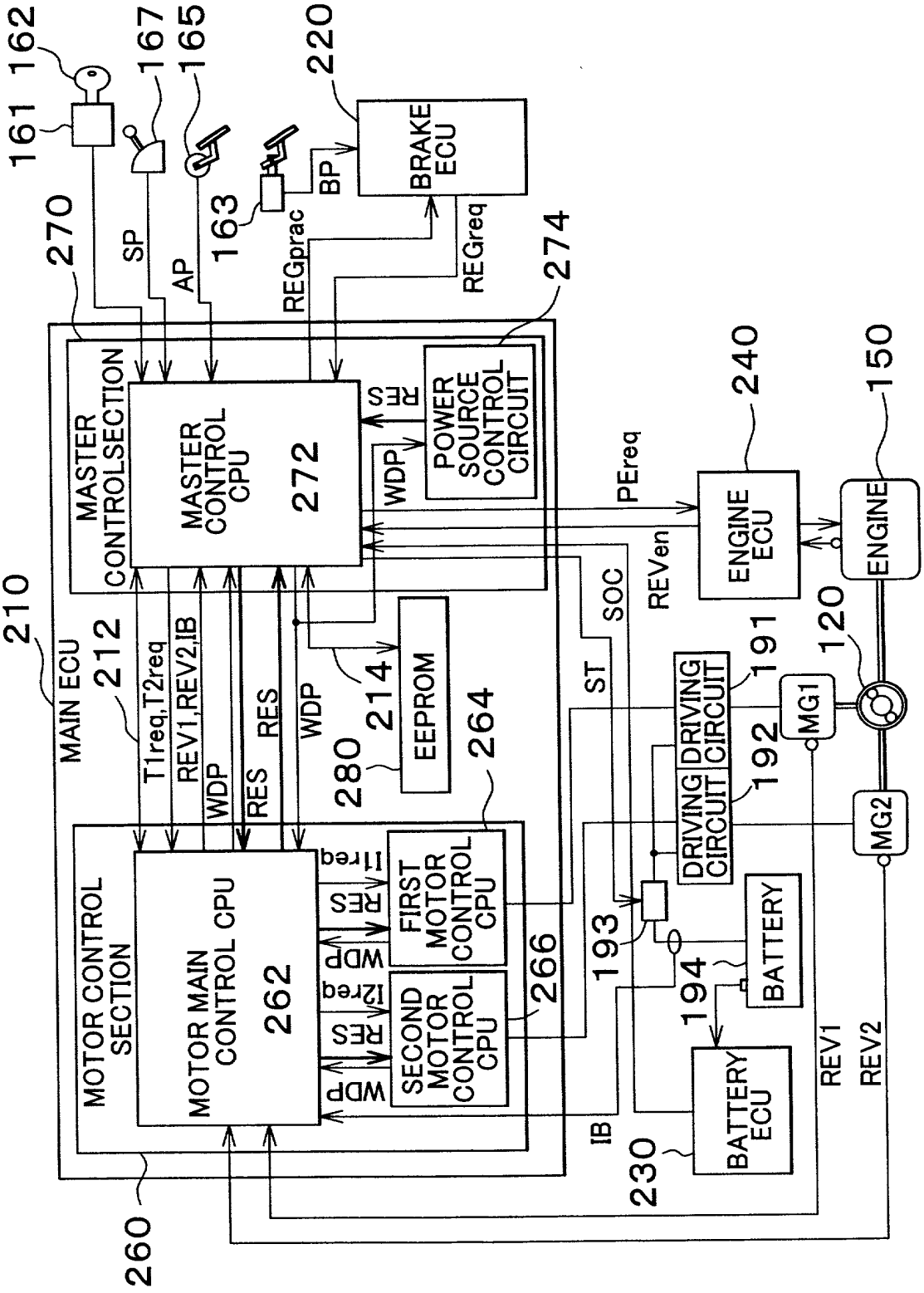

161
162
167
165
220
270
163
SP
AP
REGprac
BP
BRAKE ECU
REGreq
274
MASTER CONTROLSECTION
MASTER CONTROL CPU
272
RES
WDP
POWER SOURCE CONTROL CIRCUIT
PEreq
240
ENGINE ECU
150
ENGINE
210
MAIN ECU
212
T1req,T2req
REV1,REV2,IB
WDP
RES
RES
WDP
214
280
EEPROM
264
REVen
SOC
ST
191
DRIVING CIRCUIT
120
MG1
192
DRIVING CIRCUIT
MG2
MOTOR CONTROL SECTION
MOTOR MAIN CONTROL CPU
262
I1req
RES
WDP
FIRST MOTOR CONTROL CPU
I2req
RES
WDP
SECOND MOTOR CONTROL CPU
266
193
194
BATTERY
REV1
REV2
IB
230
BATTERY ECU
260

FIG. 3

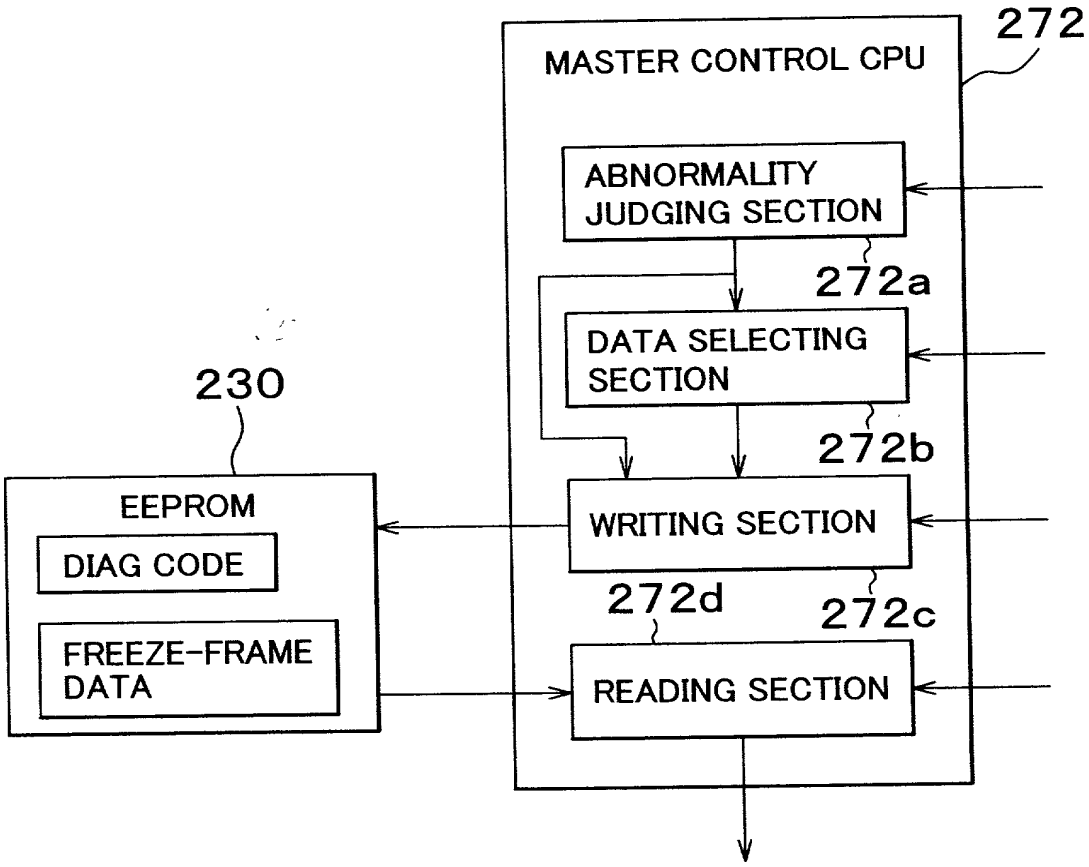

272
MASTER CONTROL CPU
ABNORMALITY JUDGING SECTION
272a
DATA SELECTING SECTION
272b
WRITING SECTION
272d
272c
READING SECTION
230
EEPROM
DIAG CODE
FREEZE-FRAME DATA

FIG. 4

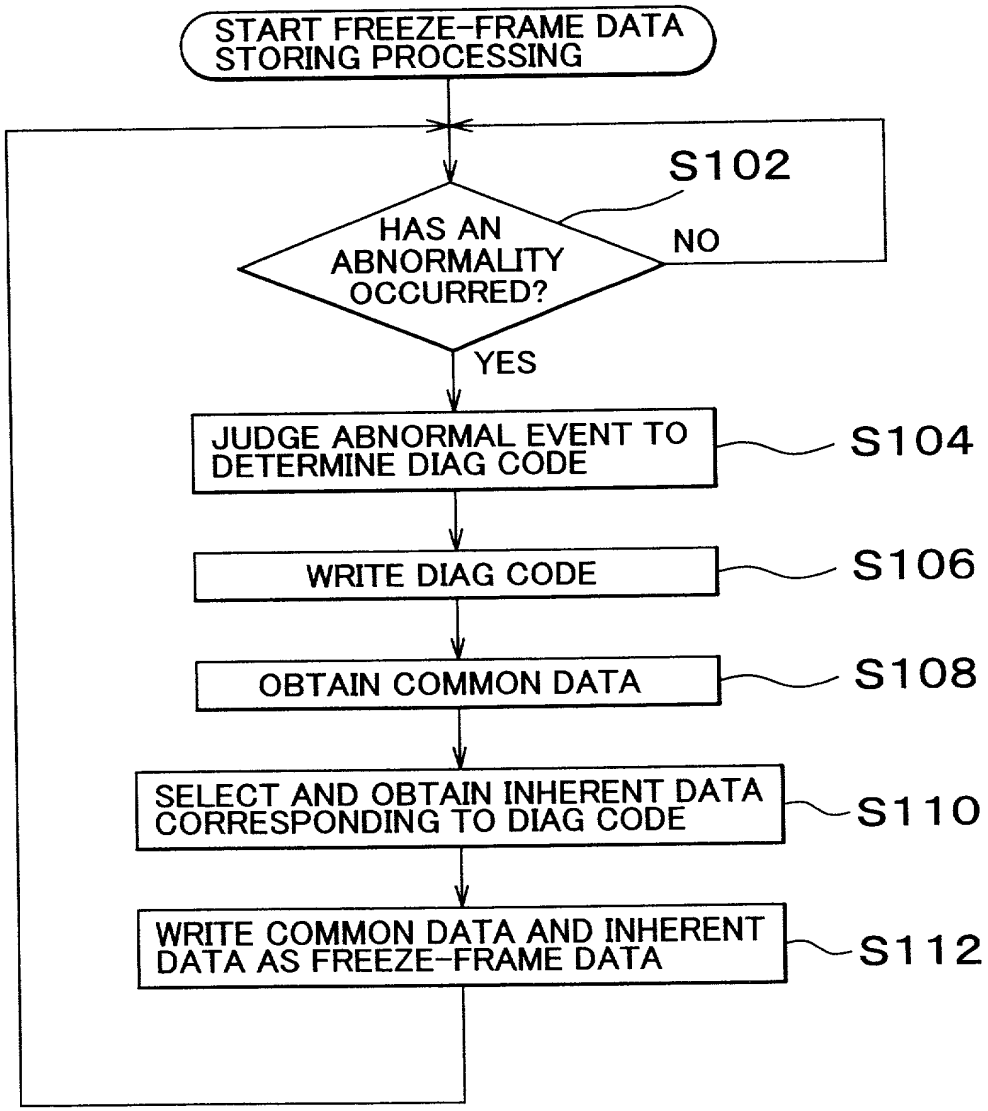

START FREEZE-FRAME DATA STORING PROCESSING
S102
HAS AN ABNORMALITY OCCURRED?
NO
YES
JUDGE ABNORMAL EVENT TO DETERMINE DIAG CODE
S104
WRITE DIAG CODE
S106
OBTAIN COMMON DATA
S108
SELECT AND OBTAIN INHERENT DATA CORRESPONDING TO DIAG CODE
S110
WRITE COMMON DATA AND INHERENT DATA AS FREEZE-FRAME DATA
S112

FIG. 5A

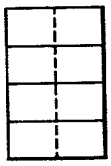


FIG. 5B

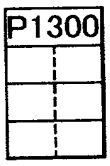


FIG. 5C

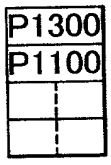


FIG. 5D

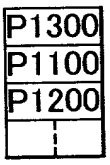


FIG. 5E

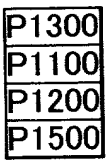


FIG. 6A

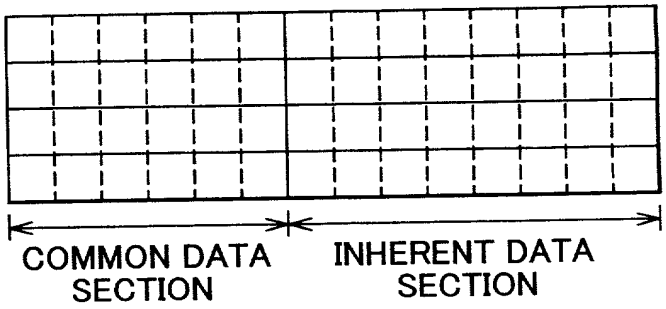


FIG. 6B

FRAME1	αt1	βt1	γt1	at1	bt1	ct1	dt1

FIG. 6C

FRAME1	αt1	βt1	γt1	at1	bt1	ct1	dt1
FRAME2	αt2	βt2	γt2	et2	ft2	gt2	ht2

FIG. 6D

FRAME1	αt1	βt1	γt1	at1	bt1	ct1	dt1
FRAME2	αt2	βt2	γt2	et2	ft2	gt2	ht2
FRAME3	αt3	βt3	γt3	it3	jt3	kt3	lt3

FIG. 6E

FRAME1	αt1	βt1	γt1	at1	bt1	ct1	dt1
FRAME2	αt2	βt2	γt2	et2	ft2	gt2	ht2
FRAME3	αt3	βt3	γt3	it3	jt3	kt3	lt3
FRAME4	αt4	βt4	γt4	mt4	nt4	ot4	pt4

FIG. 7A-1

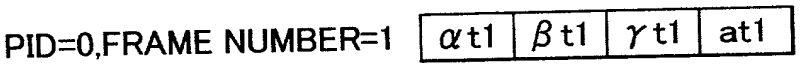


FIG. 7A-2

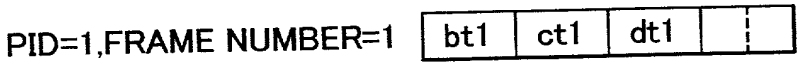


FIG. 7A-3

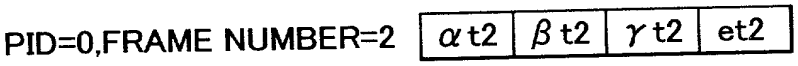


FIG. 7B-1

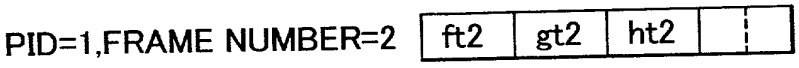


FIG. 7B-2

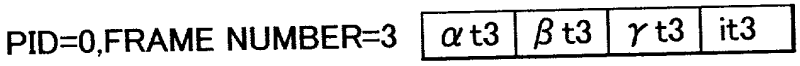


FIG. 7C-1

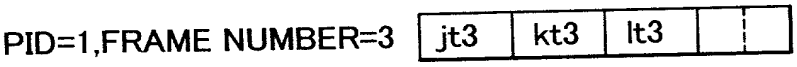


FIG. 7C-2

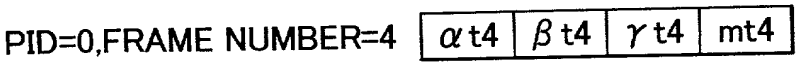


FIG. 7C-3

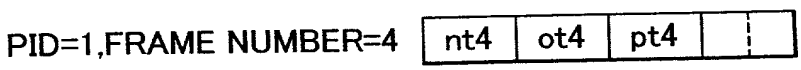


FIG. 8

FRAME1	αt1	βt1	γt1	at1	bt1	ct1	dt1
FRAME2	αt2	βt2	γt2	ft2	ht2		
FRAME3	αt3	βt3	γt3	it3	kt3	lt3	
FRAME4	αt4	βt4	γt4	mt4	nt4		

FIG. 9

FRAME1	αt1	βt1	γt1	at1	bt1	ct1	dt1
FRAME2	αt2	βt2	γt2	et2	ft2		ht2
FRAME3	αt3	βt3	γt3	it3	kt3	lt3	
FRAME4	αt4	βt4	γt4	mt4	nt4		pt4

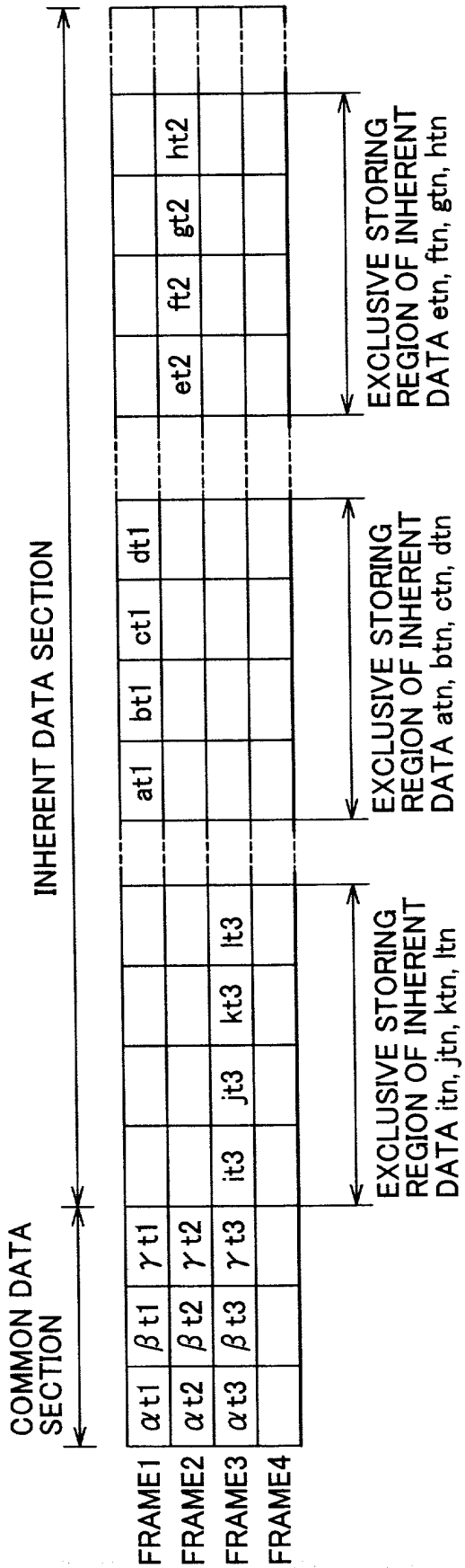
FIG. 10

FRAME1	αt1	βt1	γt1	at1	bt1	ct1	dt1
FRAME2	αt2	βt2	γt2	ft2	ht2		
FRAME3	αt3	βt3	γt3	it3	kt3	lt3	
FRAME4	αt4	βt4	γt4	mt4	nt4		

FIG. 11

FRAME1	αt1	βt1	γt1	at1	bt1	ct1	dt1
FRAME2	αt2	βt2	γt2	ft2			ht2
FRAME3	αt3	βt3	γt3	it3	kt3	lt3	
FRAME4	αt4	βt4	γt4		mt4	nt4	

FIG. 12


COMMON DATA SECTION
INHERENT DATA SECTION
FRAME1
FRAME2
FRAME3
FRAME4
αt1 βt1 γt1
αt2 βt2 γt2
αt3 βt3 γt3
it3 jt3 kt3 lt3
at1 bt1 ct1 dt1
et2 ft2 gt2 ht2
EXCLUSIVE STORING REGION OF INHERENT DATA itn, jtn, ktn, ltn
EXCLUSIVE STORING REGION OF INHERENT DATA atn, btn, ctn, dtn
EXCLUSIVE STORING REGION OF INHERENT DATA etn, ftn, gtn, htn